

10/065717 Pro
11/13/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10065717	11/13/2002	438	730	4765	

**APPLICANTS: Yang Ming-Sheng, Kao Chia, Chien Chin Cheng.

**CONTINUING DATA VERIFIED:

This application is a CIP of 09/683,691 02/04/2002

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no
ATTORNEY DOCKET NO NAUP0504USA		
Verified and Acknowledged Examiner's initials		
TITLE : Method of preventing leakage current of a metal oxide semiconductor transistor		
U.S. DEPT. OF COMM. PAT. & TM. OFF. - 5-6 Rev. 10/94		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
TERMINAL		Primary Examiner	
DISCLAIMER		Application Examiner	
PREPARED FOR ISSUE			
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